

## Compiler Construction tools

Compiler Construction tools can use Modern S/W devmt., environments containing tools such as

- language editors
  - debuggers
  - Version Managers.
  - profilers
  - text harnesses.
- and so on.
- } general S/W devmt., tools.

\* Along with the above tools, other more specialized tools have been created to help implement various phases of a Compiler.

\* These tools use ~~specific~~ specialized PLs for implementing specific components using many sophisticated and complex algorithms.

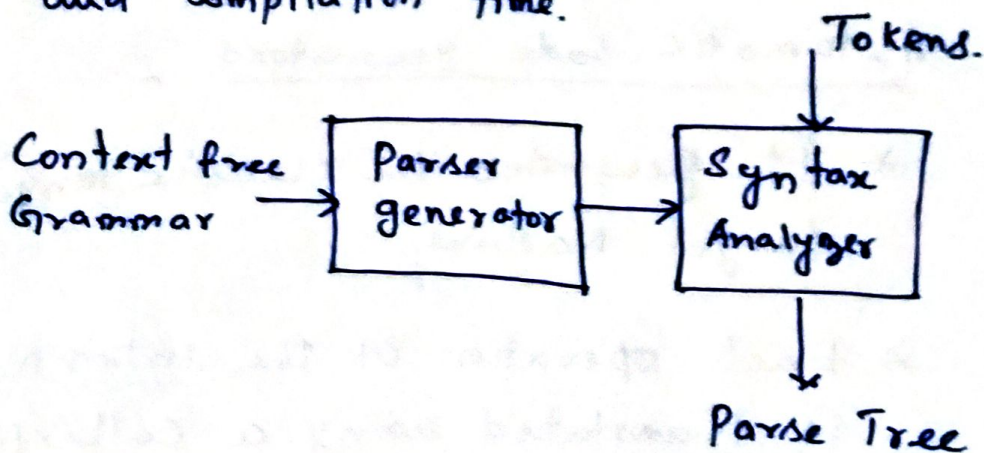
(ix) \* Most of the tools provide high degree of abstraction means the details of implementation is hidden from outside world.

# Compiler Construction tools (or) Writing tools

- \* The Compiler writer can use some specialized tools that help in implementing various phases of a Compiler.
- \* These tools assist in the creation of an entire Compiler or its parts.
- \* Some Commonly used Compiler Construction tools include.:

## 1. Parser Generator

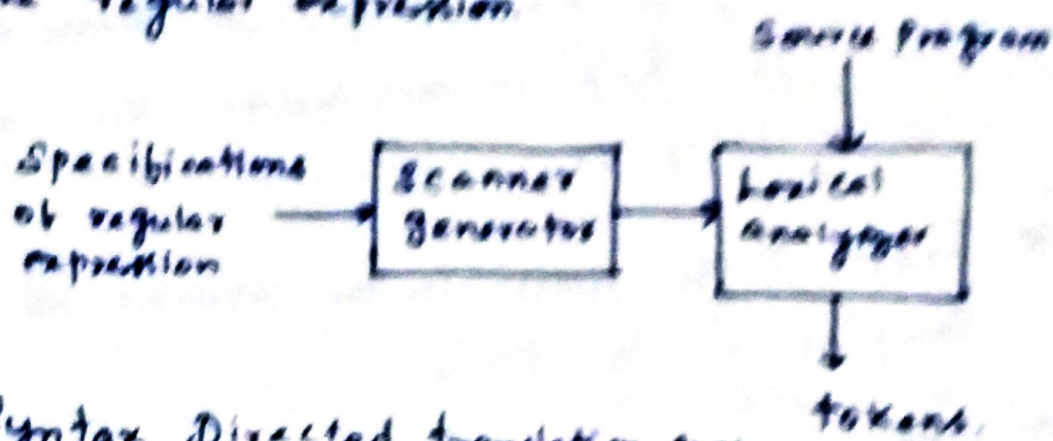
It produces syntax analyzers (parsers) from the input that is based on a grammatical description of PL or on a Context free grammar. It is useful as the Syntax analysis phase is highly complex and consumes more Manual and and Compilation time.



## 2. Scanner Generator

It generates lexical analyzers from the input that consists of regular expression

description based on tokens of a language.  
It generates a finite automaton to recognize the regular expression.



### 3. Syntax Directed translation engine.

- \* It generates intermediate code with three address format from the input that consists of a parse tree.
- \* These engines have routines to traverse the parse tree and then produces the intermediate code.
- \* In this each node of the parse tree is associated with one or more translations.

### 4. Automatic code generators

- \* It generates the machine language for a target machine.
- \* Each operation of the intermediate language is translated using a collection of rules and then is taken as an input by the code generator.

(2)

- \* A template matching process is used.
- \* An intermediate Language Statement is replaced by its equivalent Machine language Statement using templates.

#### 5. Data flow analysis engines.

- \* It is used in Code optimization.
- \* Data flow analysis is a key part of the code optimization that gathers information that is the values that flow from one part of a program to another.

#### 6. Compiler Construction toolkits.

- \* It provides an integrated set of routines that aids in building Compiler Components in the construction of various phases of Compiler.

Ex-2

x = a + b \* c ;

$l(l+d)^*$   
Pattern.

Lexical Analyser.

once the LA receives this i/p, the i/p is converted into a stream of tokens. Not only this, it removes the white spaces and skipping the comments.

id. = id + id \* id

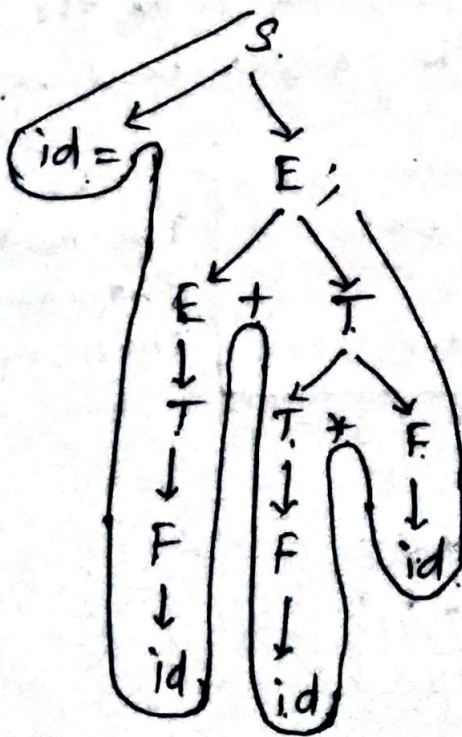
Syntax Analyser

Context free Grammar.

S → id = E ;  
E → E + T / T  
T → T \* F / F  
F → id.

rules.

Parse Tree.



The Rules the PL can be entirely represented in. Some Productions rules.

S → A statement can be. identifier = expression followed by ;

E → An expression can be Expression + Term or Term.

T → A Term can be Term \* Factor or Factor

F → can be an identifier.

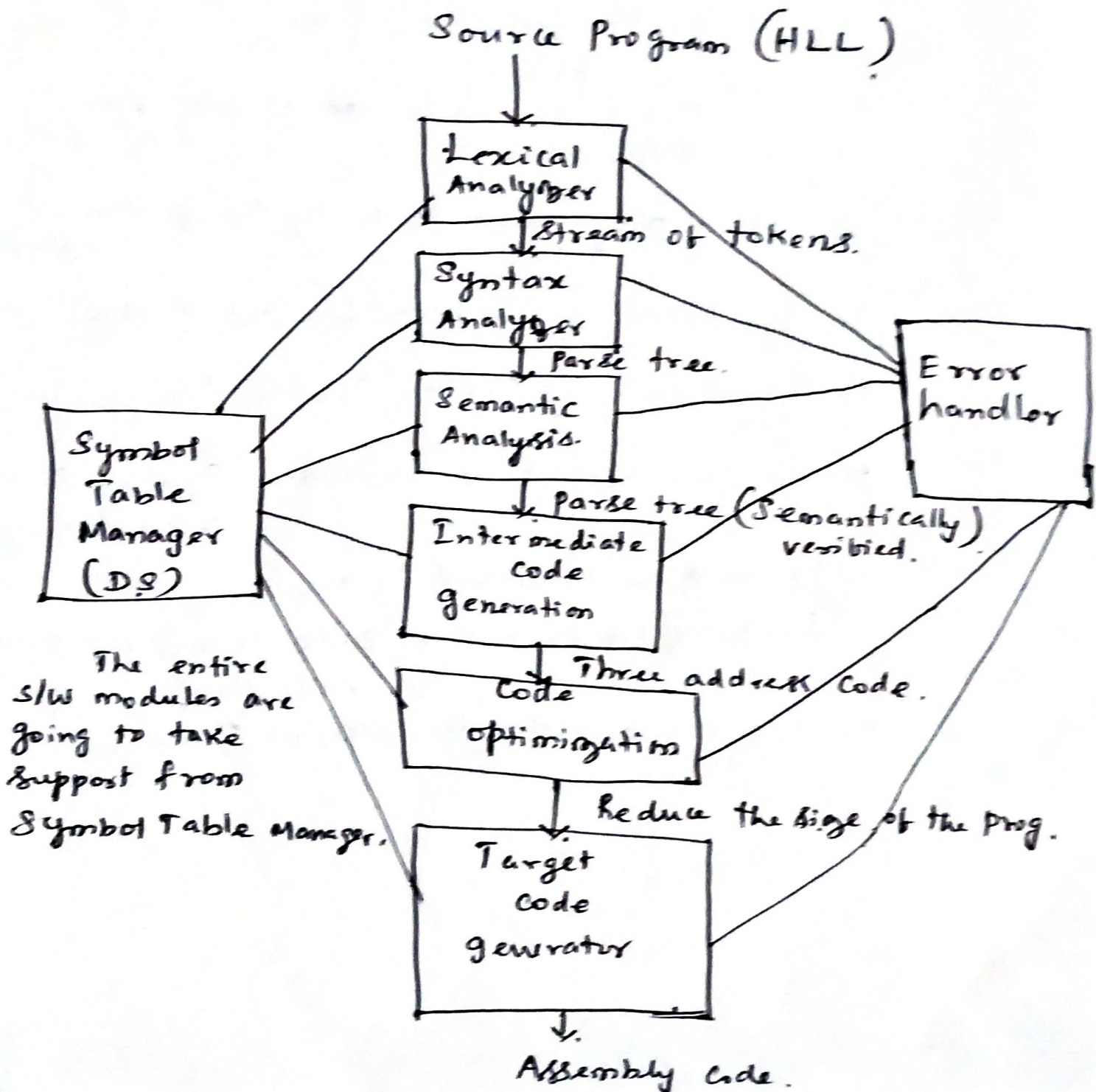
The Syntax error is detected by Syntax Analyser if the input is not according to the Grammar given.

Semantic Analyser

# Phases of Computer Compiler

## Phase?

is a logically interrelated operation that takes source prog. in one representation and produce output in another representation.

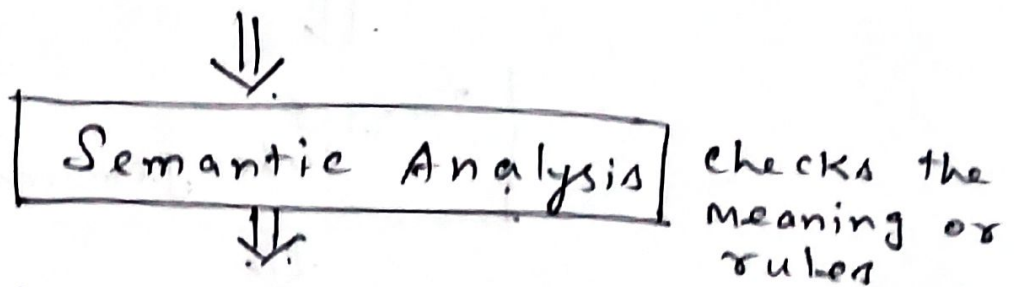
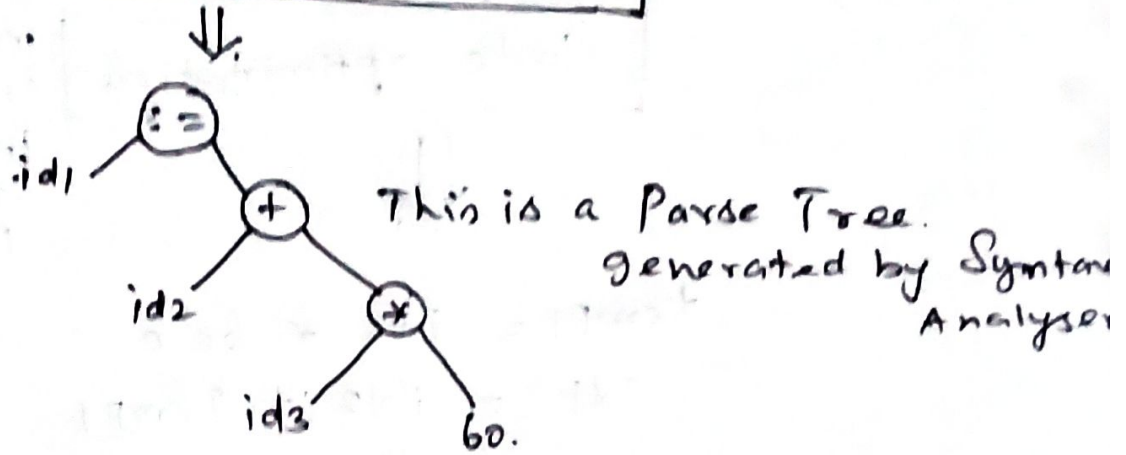
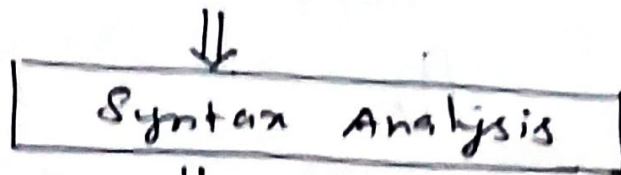
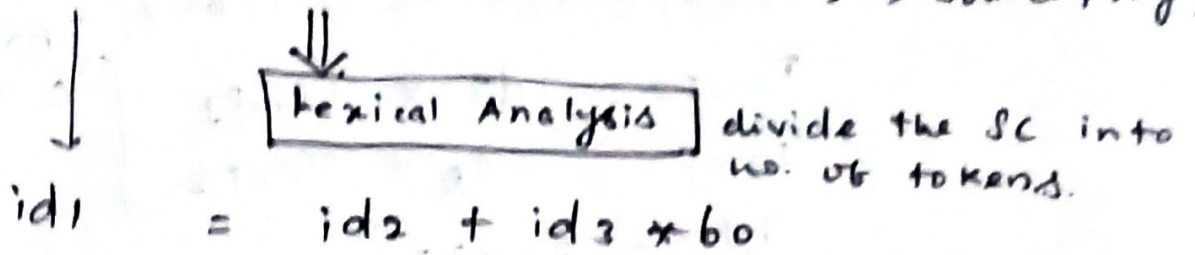


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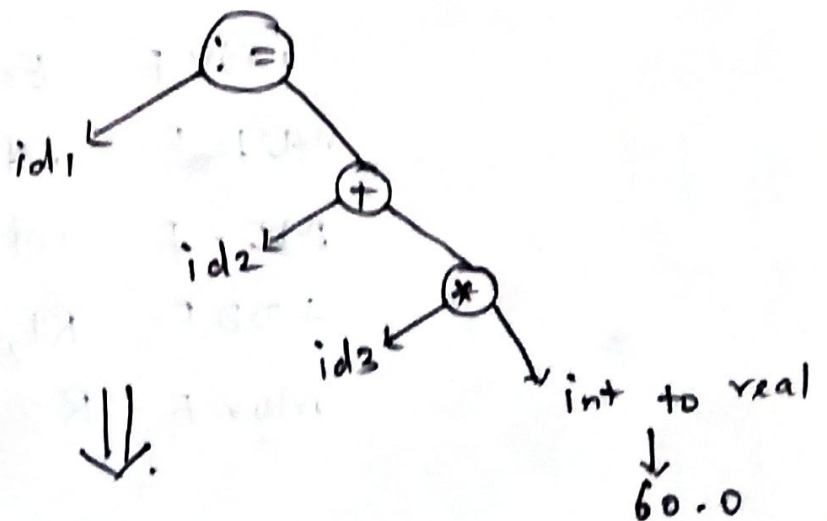
# Explanation with example

Ex

Position := initial + rate \* 60 ; → Source Prog.



Suppose rate is float type but 60 is int. So, convert 60 into float.



Intermediate  
Code  
Generation.

Generates  
a three address code.

$Temp_1 = \text{int to real}(60)$   
 $Temp_2 = id_3 * Temp_1$   
 $Temp_3 = id_2 + Temp_2$   
 $id_1 = temp_3$

Three address  
Code

Code optimization

removes unnecessary  
information to  
increase the  
speed of the  
program.

$temp_1 = id_3 * 60.0$   
 $id_1 = id_2 + Temp_1$

Target  
code generator

MOV F 60.0, R1  
MUL F id3, R1  
MOV F id2, R2  
ADD F R1, R2  
MOV F R2, id1.

Assembly  
Language  
Prog.



Ex-2

$x = a + b * c ;$   
 $id$

$(k+d)^*$   
 Pattern

Lexical Analyser

once the LA receives the I/P, the I/P is converted into a stream of tokens. Not only this, it removes the white space & and skipping the comments.

$id = id + id * id$

Syntax Analyser

Context free Grammar

- $S \rightarrow id = E ;$
- $E \rightarrow E + T / T$
- $T \rightarrow T * F / F$
- $F \rightarrow id$

Parse Tree



The rules the G can be entirely represented in some productions.

- $S \rightarrow$  A statement can be identifier = expression followed by ;
- $E \rightarrow$  An expression can be expression + Term or Term.
- $T \rightarrow$  A Term can be Term \* Factor or Factor
- $F \rightarrow$  can be an identifier.

The Syntax error is detected by Syntax Analyser if the input is not according to the Grammar given.

Semantic Analyser

Semantic Analyser is going to verify the parse tree whether the parse tree is semantically correct or not.

eg

$id = id + id * id,$

left hand side should be a variable  
 can't be a constant or can't be a fun name  
 or can't be an array name.  
 So, ~~left~~ left hand side has to be a variable  
 which is compatible with the type of  
 variable in the right hand side.

Intermediate  
 Code  
 Generator

The most popular  
 Intermediate code is  
 three address code.

$t_1 = b * c;$   
 $t_2 = a + t_1;$   
 $x = t_2;$

Code optimization → reduce the  
 no. of lin

MUL R1, R2 | a → R0  
 add R0, R2 | b → R1  
 MOV R2, X | c → R2

$t_1 = b * c;$   
 $x = a + t_1;$

T.C.G

# ① Compiler Design

## Importance of Subject

- \* Compilers provide you with the theoretical and practical knowledge that is needed to implement a Prog. Language.

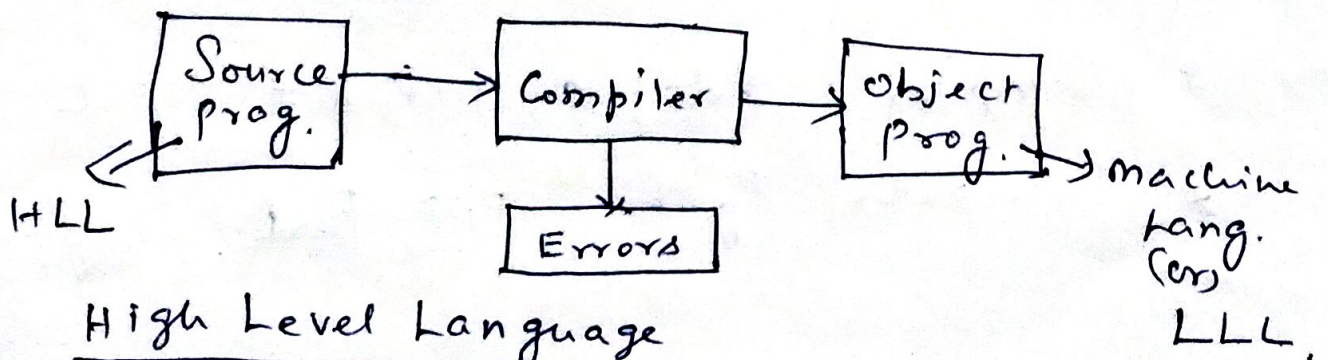
## Contents

- \* Structure of Compiler
- \* Interpreter
- \* Compiler Vs Interpreter
- \* Language Processing System.

## Introduction to Compiler.

A compiler is a translator / s/w that converts the program written in high-level language (Source Language) to Low level Language (Object / Target / Machine Language).

### Block Diagram



A Program written in English Language English Language is familiar with us to understand easily.

We can easily understand the Syntax, Semantic, and Structure.

\* Machines do not understand HLL. They can understand LLL.

So Compiler Converts HLL to LLL.

### Compiler and Interpreter:

\* To convert Source Code (SC) into machine code, we use either a Compiler or an interpreter.

\* Compiler transforms code written in a high-level Prog. Lang. into the Machine Code at once before the prog. runs.

\* Whereas an interpreter converts each high level program statement, one by one into the Machine code, during execution of prog.

\* Compiler: Ada, C, C++, C#, COBOL.

\* Interpreter: Python, PHP, Perl, Ruby.

(2)

## Compiler Vs Interpreter.

### Compiler

\* Scans the entire prog. and translates it as a whole into Machine code.

\* Take a large amt, of time to analyse the SC.

\* The overall exe, time is comparatively faster than interpreters.

\* Requires More Memory (generates intermediate code, so it requires more amts of memory)

### Interpreter.

\* Translate prog, one st, at a time.

\* Take less amt, of time to analyse the SC.

\* The overall exe, time is comparatively slower than Compilers.

\* Memory efficient, (No need to generate intermediate code.)

Have you heard any PL name which uses both Compiler and Interpreter?

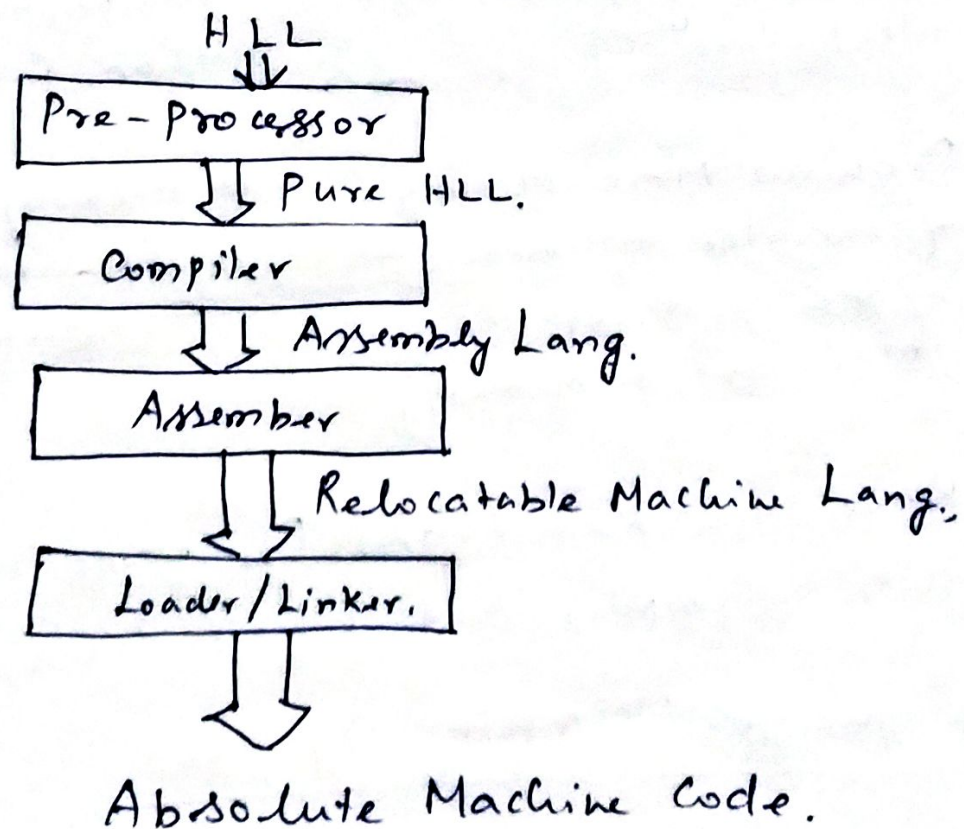
## Language Processing System.

The task of compilation will not be done by Compiler alone. There are other s/w modules which will accompany compiler in this process.

So, in order to know what are the s/w modules, we ~~need~~ need to know what is Lang., Processing System. is.

Language Processing System Consists of 4 s/w modules.

1. Pre-Processor
2. Compiler
3. Assembler
4. Loader/Linker.



Let us have a detailed descriptions about each s/w modules.

(3).

## 1. Pre-processor

The first SW module is Preprocessor whose input is HLL and output is Pure HLL.

Now what is this Pure HLL?

Consider a C program. Basically your C prog should start with a line `#include <stdio.h>` and `#include <math.h>`.

These header files are also called as Pre processor Directives

Removes :-  
Directives like  
#include  
#define  
Performs :-  
Macro  
expansion  
File inclusion

Function calling is overhead but  
Macro calling is simple

The Pre Processor is going to remove the above `#include`, `#define` by including files related to them. This is called as File inclusion

ie whatever the files you want to include Preprocessor is going to substitute that the entire files in your source prog.

Pre processor will also do Macro expansion

## 2. Compiler.

The input to the Compiler is Pure HLL which means the prog. will not contain any # lines (or) tags.

Compiler is going to Convert Pure HLL into assembly Language.

This Assembly Lang. is not entirely 0's and 1's and not entirely HLL.

It is somewhat intermediate.

## 3. Assembler

Assemblers of one platform will not work with any other platform

The o/p of the Assembler will be machine codes.

Basically Machine codes of two types.

1. Relocatable Machine code
2. Absolute Machine code.



(4)

Relocatable Machine Code is loaded at any point of your computer, and you can run it.

This ~~let~~ Relocatable Machine Code is given as input to the next phase Loader/linker.

#### 4. Loader/Linker.

The linker will link variety of object files into one file and the loader will load this ~~into~~ files into the memory.

By this the execution of prog will be completed.

Now we will focus on Compiler and not on all SW modules.

Before discussing the phases of a compiler, let us know something about

- \* word
- \* Sentences
- \* Languages

\* Word can be defined as a set of chars. which gives a meaning.

eg "boy" → word.  
→ defined from the set of chars from the alphabets available in English Lang.  
→ It denotes Masculine gender child.

\* Sentences

is a set of words which gives a meaning.

eg "The boy is running"

- The sentence should follow the grammar
- Here the Grammar defines the way in which the sentence can be formed.
- If a sentence does not follow any Grammar, ~~an~~ then it can be called as a grammatically incorrect sentence.

\* Languages

- The Lang, is a tool used to Communicate with others
- It is defined over the sentences in turn words, which in turn defined over the chars.

(5)

The following example will help us in understanding concepts of Compiler Construction.

Consider the following sentence in English.

I AM GOING TO MARKET.

I → Subject

AM → Auxiliary verb.

Going → Verb + ing

TO MARKET → object.

This simple sentence is Syntactically,  
Grammatically Correct.

eg The boy is going to hastel  
↓  
Syntax error.  
↳ This sentence is not syntactically correct.

eg The boy is go to hastel.  
↓  
Grammatically incorrect.

~~also~~ We have a sentence which is syntactically and grammatically correct.

"The boy is going to hostel"

Now what is to be done?

The answer is, The boy must do some action.

ie The boy must go to the hostel.

The action is attached to the sentence

-x. It is clear that an action is associated with a sentence which is syntactically, grammatically correct.

The above simple analogies will explain how the ~~computer~~ compiler works in very simpler terms.

Language → Any HLL.

word → string

Sentence → statement in a HLL.

set of sentences → set of statements  
ie a prog. in a HLL.

To carry out certain task a sentence must be written, which should satisfy the foll:

1. Syntactically Correct
2. Grammatically Correct
3. An Action must be associated
4. It must be understood by the Executor for execution.

"<sup>(or)</sup> must be made ~~be~~ ready for execution."

The st<sub>s</sub> of a prog. must satisfy the foll: in a Compiler.

1. Syntactically Correct (Lexical Analyser)
2. Grammatically Correct (Syntax Analyser)
3. An action must be associated (Syntax Direct Translation)
4. It must be understood by the Executor for execution (Code generation and execution)

\* Code optimization is optional phase.

## Symbolic Assembly Language

- \* The most immediate step away from machine language is Symbolic Assembly Language.
- \* In this Language, a programmer uses Mnemonic names for both operation code and data addresses.

\* Thus a programmer could write

$$\begin{array}{ccc} \text{ADD } X, Y. & \rightarrow & \text{Assembly Language,} \\ \Downarrow & \Downarrow & \swarrow \\ \text{instead of } 0110 & 001110 & 010101 \rightarrow \text{Machine Language.} \end{array}$$

- \* A Computer can't execute a program written in assembly Language.
- \* That program has to be first translated to Machine Language, which the Computer can understand. The program that performs this translation is the assembly, Assembler.

## Macros

- \* A Macro facility is a text replacement capability.
- \* There are two aspects to Macros.
  - definition
  - Use.

To illustrate the utility of Macros, Consider the foll. situation.

Macro definition with two-address ADD inst.

```
MACRO      ADD2  x, y
           LOAD  y
           ADD   x
           STORE y
ENDMACRO.
```

} These three stas define the Macro. ie they give its translation.

ADD2 → name of the MACRO.

x, y → dummy arguments (formal parameter)

Having Defined ADD2 in this way, we can then use it as an ordinary assembly Lang. OP-code.

For example

If the st., ADD2 A, B is encountered somewhere after the definition of ADD2, we have a Macro Use.

Here the Macro processor substitutes for ADD2 A, B the three st., which form the definition of ADD2, but with the actual parameters A and B replacing the formal parameters x and y respectively.

That is ADD2 A, B is translated to

LOAD B → Moves a datum from memory to a register.  
 ADD A → Adds the contents of a memory address to that of a register.  
 STORE B → moves data from a register to memory.

### Boots trapping

- a process by which simple language is used to translate more complicated program, which in turn may handle for ~~an even~~ more complicated program. This complicated program can further handle even more complicated prog. and so on.

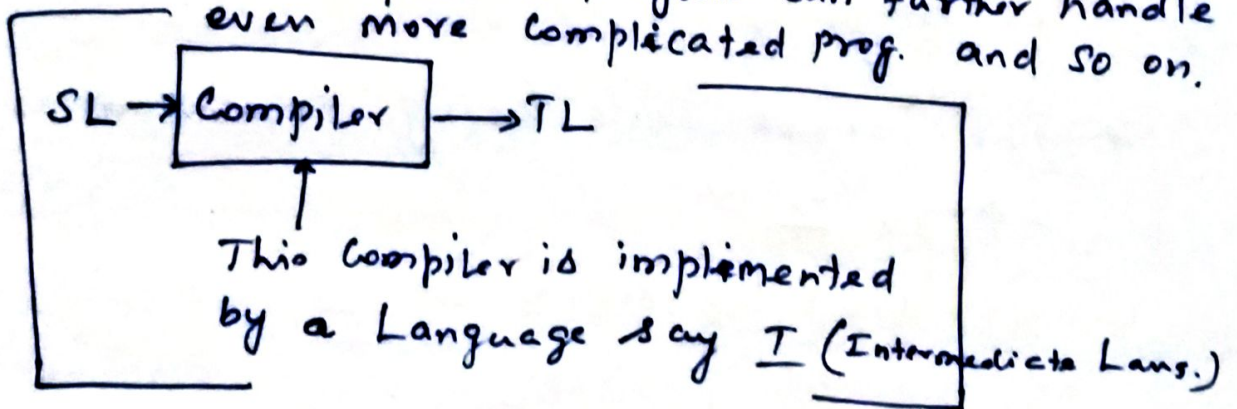


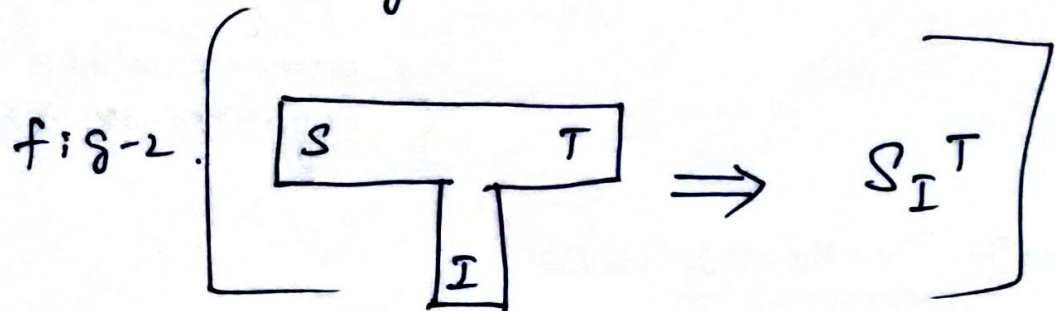
fig-1

- \* Writing a compiler for any high level language is a complicated process.
- \* It takes lot of time to write a compiler from scratch.
- \* Hence simple lang, is used to generate target code in some stages.



\* To clearly understand the Bootstrapping tech,  
Consider the foll. scenario.

\* The above fig-1 is represented as T diagram.



\* Suppose we want to write a cross Compiler  
for new language X.

\* The implementation Lang., of this Compiler  
is say Y.

\* The target code being generated is in  
Lang., Z.

(ie) We create XYZ.

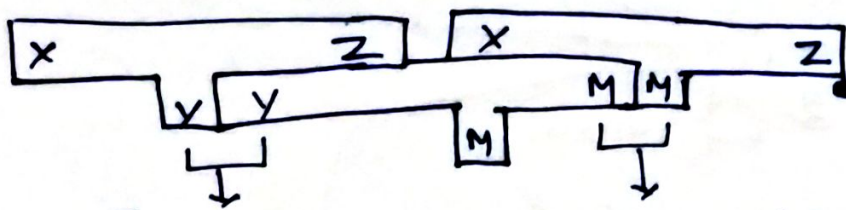
\* Now if existing Compiler Y runs on Machine  
M. and generates code for M.

Then it is denoted as YMM.

\* Now if we run XYZ using YMM then we  
get a Compiler XMZ

\* That means a compiler for source Lang., X that  
generates a target code in lang., Z. and which  
runs on Machine M.

The foll. diagram illustrates the above scenario.



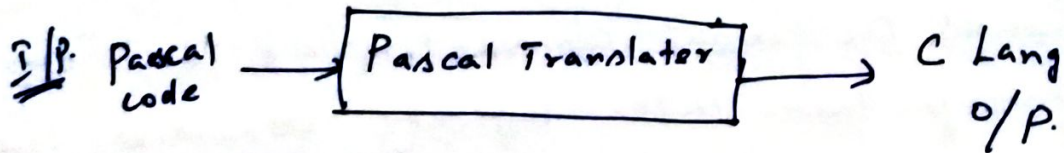
These two Languages must be same

These two Lang; must be same.

eg

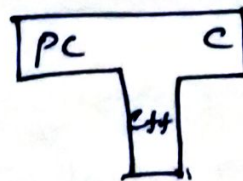
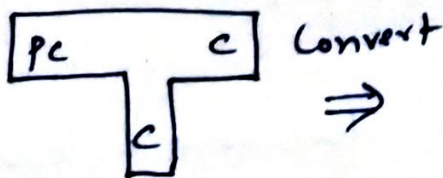
We have a Pascal Translator which is written in C Lang.

Pascal Translator - C Lang.



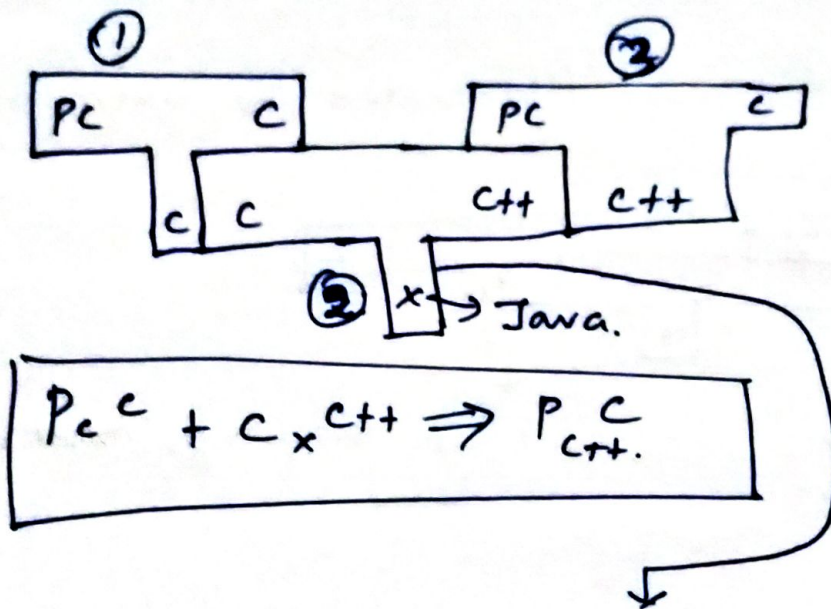
Create a Pascal Translator in C++

How will we do it?



Pascal translator written in C Lang and takes Pascal code as input and produce C Lang, as output.

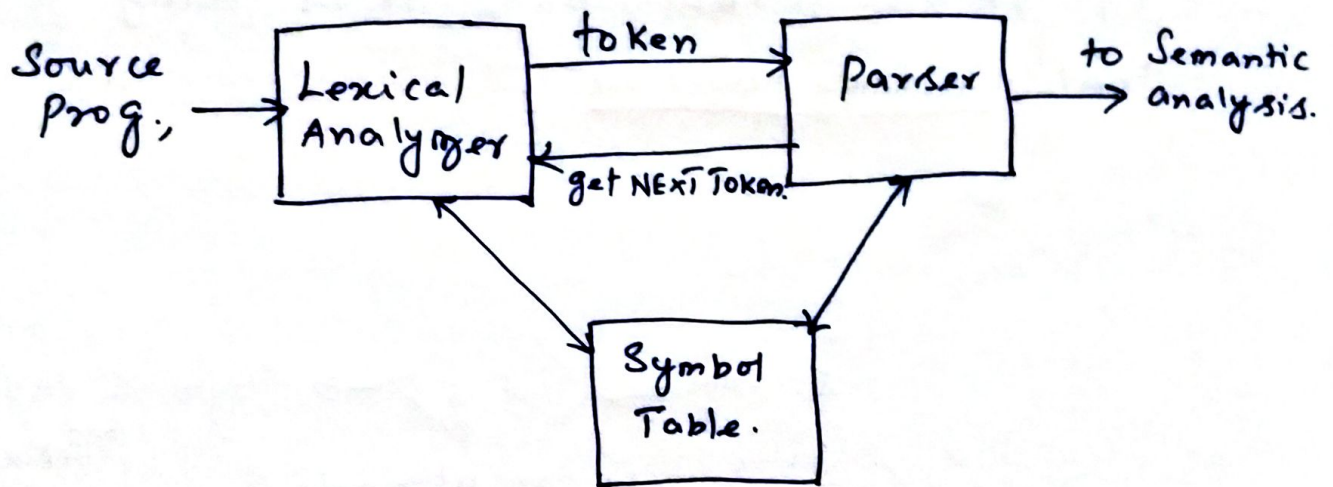
Pascal translator written in C++ and takes Pascal code as input and produce C Lang, as output.



To Convert First T diagram to Third T diagram, we should process Second T diagram.

## Role of Lexical Analyser

- \* Reads the input characters of the Source Prog. group them into logically cohesive elements called Lexemes.
- \* Produces a Seq., of tokens for each lexeme in the source program as output.
- \* when the lexical analyser discovers a lexeme constituting an identifier, it enters that lexeme into the Symbol table.



- ① Source Prog; is the input to Lexical Analyser.
- ②. Lexical Analyser reads Source Prog., char by char until a meaningful token is found.
- ③ The output of the Lexical Analyser is token.
- ④. The Lexical Analyser is accompanied a Symbol Table.
- ⑤ Whenever the LA identifies an identifier in the Source Prog., it has to check that the identifier is already ~~and~~ existed in the Symbol table or not.

When it reads an identifier SUM, ~~we~~ we have to check <sup>whether</sup> ~~that~~ the SUM is already encountered in the Prog.